## FORMAL DRAWINGS

FIG. 1

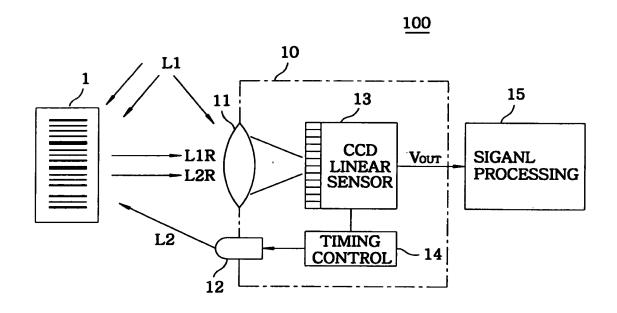


FIG.2

<u>13</u>

130 TG2
TG1
SCK

133A
134
132B
131
132A
A
A
A
B
Vour

FIG.3A	OPERATION MODE	CHAGE ACCUMULATION SIGNAL READING PERIOD
FIG.3B	ON/OFF CONTROL SIGNAL	
FIG.3C	EXPOSURE PERIODS OF ON-STATE	
FIG.3D	EXPOSURE PERIODS OF OFF-STATE	
FIG.3E	TRANSFER TIMING (TG1) FOR FIRST TRANSFER GATE	
FIG.3F	TRANSFER TIMING (TG2) FOR SECOND TRANSFER GATE	
FIG.3G	SIGNAL READING TIMING (SCK)	
FIG.3H	OUTPUT SIGNAL(Vout)	000000

## FIG. 4A

SIGNAL CHARGE FOR INCIDENT LIGHT L1R

SIGNAL CHARGE FOR INCIDENT LIGHT L2R OUTPUT OF FIRST CCD SHIFT REGISTER

FIG.4B

SIGNAL CHARGE FOR INCIDENT LIGHT L1R OUTPUT OF SECOND CCD SHIFT REGISTER

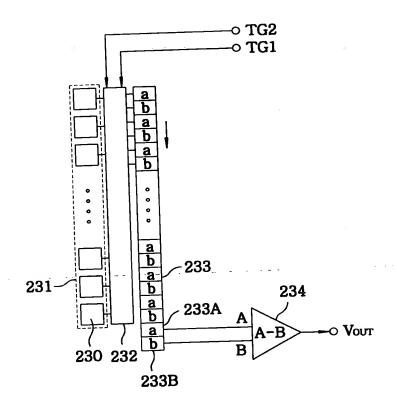
FIG.4C

SIGNAL CHARGE FOR INCIDENT LIGHT L2R

OUTPUT OF DIFFERENTIAL AMPLIFIER

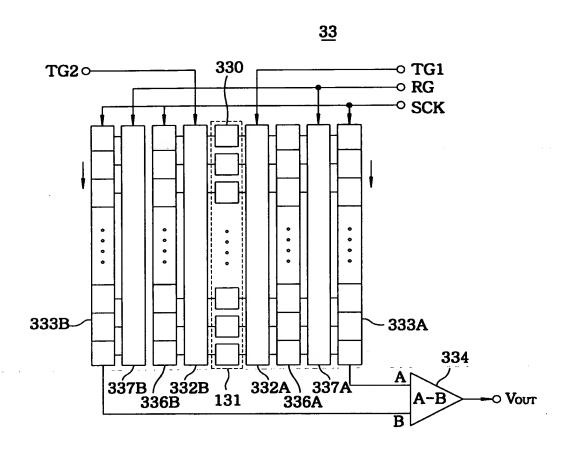
FIG.5

<u>23</u>



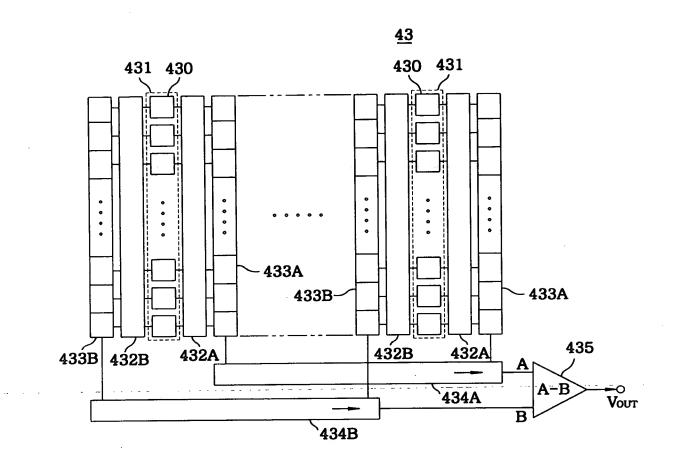
	al bl a2 b2	al bl a2 b2 a3	a1-b1	
SIGNAL READING TIMINING FOR CCD 233	OUTPUT SIGNAL OF CCD 233A	OUTPUT SIGNAL OF CCD 233B	OUTPUT SIGNAL OF DIFFERENTIAL AMPLIFIER	OUTPUT VOLTAGE OF DIFFERENTIAL AMPLIFIER
FIG.6A	FIG.6B	FIG.6C	FIG.6D	FIG.6E

FIG.7



FIRST CHAGE ACCUMULATION FIRST READING PERIOD SECOND READING SECOND CHARGE ACCUMULATION PERIOD								
OPERATION MODE	ON/OFF CONTROL SIGNAL	EXPOSURE PERIODS OF ON-STATE	EXPOSURE PERIODS OF OFF-STATE	TRANSFER TIMING (TG1) FOR FIRST TRANSFER GATE	TRANSFER TIMING (TG2) FOR SECOND TRANSFER GATE	READING GATE TRANSFER TIMING (RG)	SIGNAL READING TIMING (SCK)	OUTPUT SIGNAL(Vour)
FIG.8A	FIG.8B	FIG.8C	FIG.8D	FIG.8E	FIG.8F	FIG.8G	FIG.8H	FIG.8I

FIG.9



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FIG. 10

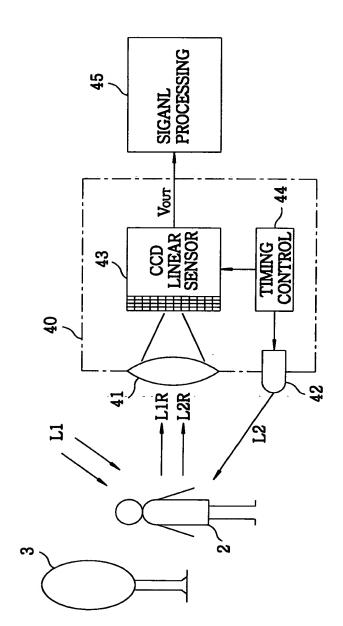


FIG. 1 1A

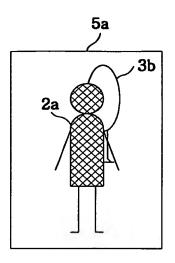


FIG. 11B

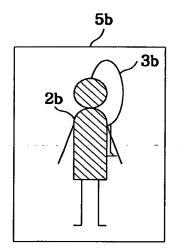


FIG. 11C

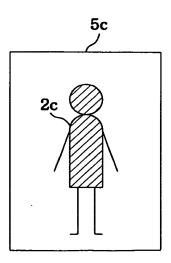
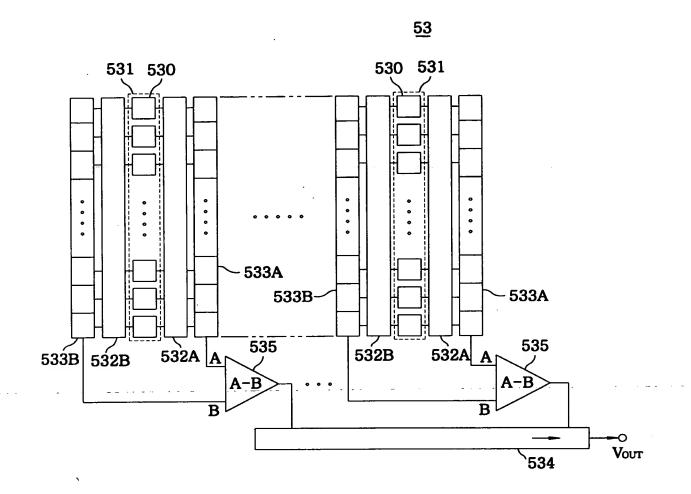


FIG. 12



CHAGE ACCUMULATION SIGNAL READING PERIOD PERIOD							
OPERATION MODE	ON/OFF CONTROL SIGNAL	EXPOSURE PERIODS OF ON-STATE	EXPOSURE PERIODS OF OFF-STATE	TRANSFER TIMING FOR FIRST TRANSFER GATE	TRANSFER TIMING FOR SECOND TRANSFER GATE	SIGNAL READING TIMING (SCK)	OUTPUT SIGNAL(Vout)
FIG. 13A	FIG. 13B	FIG. 13C	FIG. 13D	FIG. 13E	FIG.13F	FIG. 13G	FIG.13H

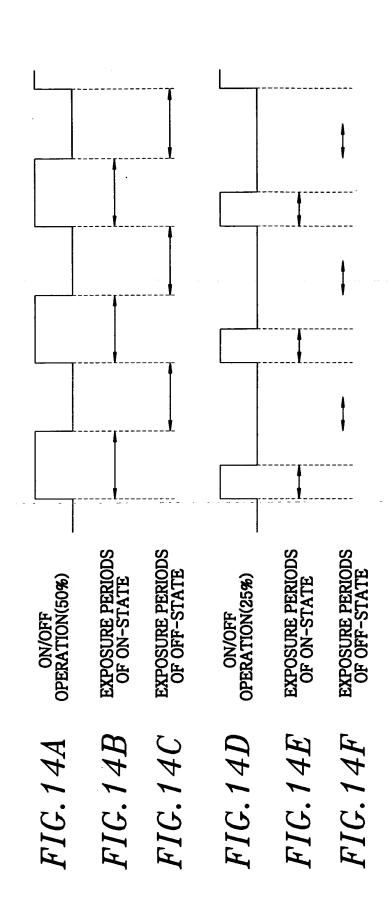


FIG. 15

